

Marked up Version to Show Changes MadeIN THE ABSTRACT

Amend the ABSTRACT as follows.

~~A trigger circuit (22) having a depletion mode n type transistor (32) and a depletion mode p type transistor (34) operate by having each gate thereof driven by an independent source. When both transistors are on, the depletion mode n type transistor (32) is driven by  $I_{a1}$  to  $V_{supply}$  and the depletion mode p type transistor (34) is driven by  $I_{a2}$  to ground. When both transistors are off, a transistor (26) is switched on driving  $I_{a1}$  to ground, and a transistor (28) is switched on driving the gate of depletion mode p type transistor (34) to  $V_{supply}$ . A linear regulator (50) using a depletion mode transistor pair (52, 54) with their gates thereof driven by separate sources provides a low voltage operation with minimal current leakage. One depletion mode transistor (52) is an n type, and the second depletion mode transistor (54) is a p type transistor.~~

An amplifier (170) includes first and second depletion mode transistors (161, 162) operating in response to first and second complementary signals ( $V_{AMP+}$ ,  $V_{AMP-}$ ), respectively, which route a first current ( $I_{STACK1}$ ) from a first supply terminal (171) to an output (169) of the amplifier. Third and fourth depletion mode transistors (163, 164) receive the first and second complementary signals to route a second current ( $I_{STACK2}$ ) from a second supply terminal (Ground) to the output. The first and second currents are summed to produce an output signal ( $V_{AMP2}$ ).